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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Luan C. Tran

Examiner: Scott B Geyer

Patent No.: 7,285,468

Group Art Unit: 2812

Issue Date: October 23, 2007

Docket No: 2001-1158.04/US

Title: METHODS OF FORMING SEMICONDUCTOR CONSTRUCTIONS

ATTN: Certificate of Correction Branch
Commissioner for Patents
Office of Patent Publication

Certificate
NOV 07 2007
of Correction

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Respectfully submitted,

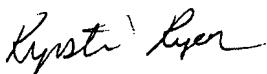
Date: October 31, 2007

By: 
Russ Slifer
Reg. No. 39,838

Kyrstin Ryan

Name

Signature



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REQUEST FOR CERTIFICATION OF CORRECTION

ATTN: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

As the error is that of the Patent Office, it is believed that no fee is due.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

Respectfully submitted,

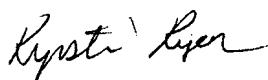
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Kyrstin Ryan



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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,285,468

Page (1) of 1

DATED : October 23, 2007

INVENTOR(S) : Tran

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the face page, in field (56), under "U.S. Patent Documents", in column 1, line 7, below "4,937,756 A 6/1990 Hsu et al." insert - - 4,960,725 10/1990 Noguchi - -.

On the face page, in field (56), under "U.S. Patent Documents", in column 2, line 2, below "5,010,518 A 4/1991 Toda" insert - - 5,027,171 6/1991 Reedy et al. - -.

On the face page, in field (57), under "Abstract", in column 2, line 6, Delete "subs regions." and insert - - sub-regions. - -, therefor.

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Nov 7 2007